

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Ref
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20050001261 A1	20050106	16	Nonvolatile semiconductor memory device	257/316		
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20040166630 A1	20040826	34	Stitch and select implementation in twin MONOS array	438/257		
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20020168813 A1	20021114	34	Stitch and select implementation in twin MONOS array	438/200		
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6759290 B2	20040706	31	Stitch and select implementation in twin MONOS array	438/200	365/185.02; 365/185.18; 365/185.27	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6707079 B2	20040316	40	Twin MONOS cell fabrication method and array organization	257/288	257/296; 257/906; 257/E71.427	
6	<input type="checkbox"/>	<input type="checkbox"/>	JP 2004266085 A	20040924	18	NONVOLATILE SEMICONDUCTOR MEMORY DEVICE			
7	<input type="checkbox"/>	<input type="checkbox"/>	JP 2004266084 A	20040924	17	NONVOLATILE SEMICONDUCTOR MEMORY			